

## PRODUCT BRIEF

### HPC SoC Platform\*

SEMIFIVE SoC Platforms can quickly turn your critical IPs or winning specifications into fully functioning SoC at a fraction of risk, time, and effort. The HPC SoC Platform offers the best solution to build custom high-performance compute, cloud servers, hyperscale data centers, enabled with silicon-proven design components on Samsung 5nm process and extensive hardware/software environments to instantly get your chip ready for systems.

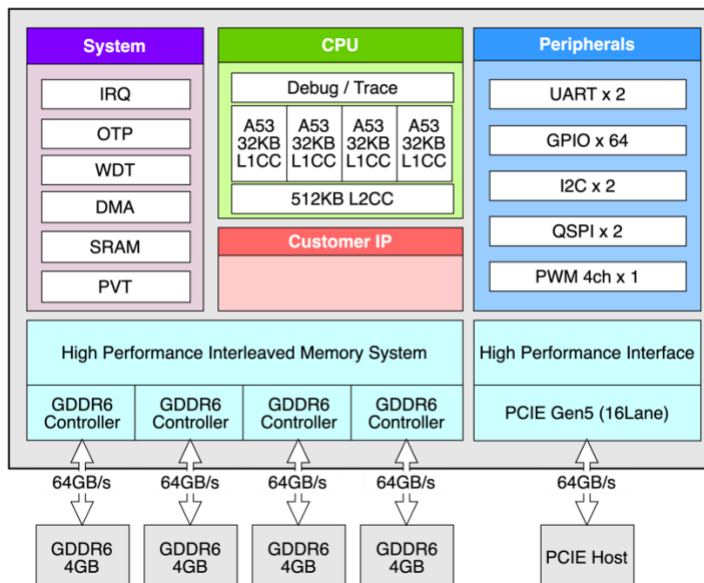
#### Highlights

- Optimized for high performance compute and advanced AI acceleration
- Support latest interfaces PCIe Gen 5 and GDDR6
- Built using Samsung Foundry's state of art 5nm FinFET (EUV enabled) process technology
- Complete solution with:
  - Package design and implementation
  - Evaluation board
  - Software and drivers

#### Target Applications

- Hyperscale Data Centers
- Cloud Servers
- Network Processors
- High performance AI accelerators

#### Block diagram



\* Note: Features listed are subject to change. Contact SEMIFIVE for the latest updates



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## Key features\*

<b>Process node</b>	<ul style="list-style-type: none"> <li>• Samsung Foundry 5nm FinFET</li> </ul>
<b>Die size</b>	<ul style="list-style-type: none"> <li>• 10.8mm x 13.8mm</li> </ul>
<b>Target operating frequency</b>	<ul style="list-style-type: none"> <li>• 1.5 GHz</li> </ul>
<b>CPU core</b>	<ul style="list-style-type: none"> <li>• Quad-core Cortex A53 (@1.5GHz)</li> <li>• Include Debug &amp; Trace</li> </ul>
<b>Memory interface</b>	<ul style="list-style-type: none"> <li>• GDDR6</li> <li>• x32 4ch @16Gbps</li> </ul>
<b>I/O Interfaces</b>	<ul style="list-style-type: none"> <li>• PCIe Gen 5 x16 Lanes</li> <li>• Off-chip component <ul style="list-style-type: none"> <li>○ SPI x6 CH</li> <li>○ I2C x2 CH (up to 3.4MHz)</li> </ul> </li> <li>• Timer includes PWM x 4CH</li> <li>• UART x2 CH</li> <li>• Configurable GPIOs</li> </ul>
<b>System controller</b>	<ul style="list-style-type: none"> <li>• Interrupt Controller</li> <li>• DMA 32 Channel</li> <li>• Watch-dog Timer 32bit counter</li> <li>• 12b PVT (Power, Voltage &amp; Temperature) monitor</li> <li>• 32Kb OTP</li> </ul>
<b>Software</b>	<ul style="list-style-type: none"> <li>• Yocto-based Linux build system</li> <li>• Boot process and Linux device driver</li> <li>• Reference Linux BSP for the evaluation Board</li> <li>• DDR tuning S/W</li> <li>• Debug solutions (GDB/TRACE32)</li> </ul>

## SoC Platform Engagement Models

### Max Efficiency Model

By reusing the platform architecture and feature subblock tailored for this domain, customers can focus on their differentiation and maximize efficiency of SoC development. Perfect for SoC prototyping or high-value applications that require super-fast time-to-market speed.

### Max Flexibility Model

SEMIFIVE Platform technology offers flexibility to configure the platform architecture to your application's special requirements. SEMIFIVE can also develop new subblock design to add features necessary for the application. It's the best way to explore the trade-offs between time and cost.

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