

## PRODUCT BRIEF

### AIoT SoC Platform

SEMIFIVE SoC Platforms can quickly turn your critical IPs or winning specifications into fully functioning SoC at a fraction of risk, time, and effort. The AIoT SoC Platform offers the best solution to build custom IoT and edge processing devices, enabled with silicon-proven design components on Samsung 14nm process and extensive hardware/software environments to instantly get your chip ready for systems.

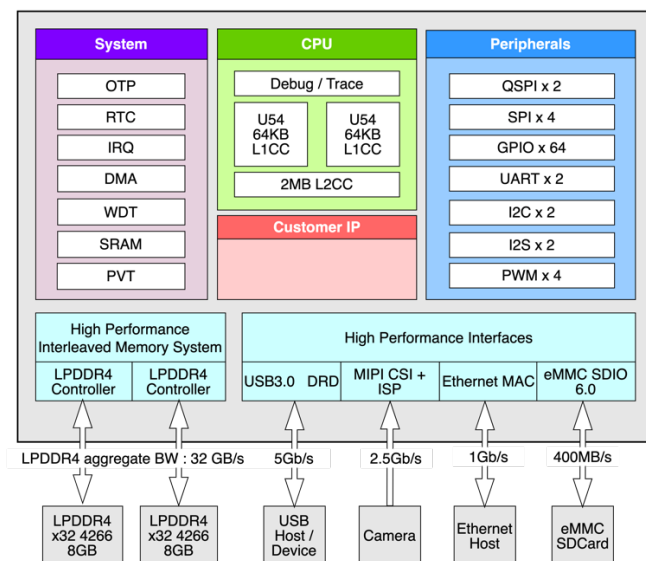
#### Highlights

- Optimized for high performance, power efficient AIoT applications
- Built using Samsung Foundry’s mass production proven 14nm FinFET process technology
- Complete solution with:
  - Package design and implementation
  - Evaluation board
  - Software and drivers

#### Target Applications

- Consumer (e.g., wearables)
- Surveillance and Smart Security
- Smart home incl Voice processing with AI
- Industrial IoT (incl smart factory)
- Drone
- Robotics

#### Block diagram



## Key features

<b>Process node</b>	<ul style="list-style-type: none"> <li>• Samsung Foundry 14nm</li> </ul>
<b>Die size</b>	<ul style="list-style-type: none"> <li>• 3.7 x 3.7mm<sup>2</sup> (Estimated, excluding customer IPs)</li> </ul>
<b>Target operating frequency</b>	<ul style="list-style-type: none"> <li>• 900 MHz</li> </ul>
<b>CPU core</b>	<ul style="list-style-type: none"> <li>• Dual Core SiFive U54 RISC-V (up to 1GHz)</li> <li>• Include Debug &amp; Trace</li> </ul>
<b>Memory interface</b>	<ul style="list-style-type: none"> <li>• 2 channel LPDDR4 (4GB ~ 16GB)</li> <li>• Up-to 4266MHz</li> </ul>
<b>I/O Interfaces</b>	<ul style="list-style-type: none"> <li>• USB 3.0 DRD</li> <li>• SNOR Flash memory with XIP support</li> <li>• Off-chip component (SPI/I2C)</li> <li>• MIPI Camera I/F</li> <li>• I2S Audio I/F</li> <li>• SD Card &amp; eMMC memory</li> <li>• Timer includes PWM</li> <li>• UART</li> <li>• GPIO</li> </ul>
<b>NPU</b>	<ul style="list-style-type: none"> <li>• 1.0 TOPS for DL Inference Acceleration (Optional)</li> </ul>
<b>Multimedia</b>	<ul style="list-style-type: none"> <li>• 8M-pixel ISP</li> </ul>
<b>Low power features</b>	<ul style="list-style-type: none"> <li>• Clock &amp; Power gating controlled by S/W</li> <li>• Under 5uA @ Always On block</li> </ul>
<b>System controller</b>	<ul style="list-style-type: none"> <li>• RTC</li> <li>• Interrupt Controller</li> <li>• DMA</li> <li>• Watch-dog Timer</li> <li>• PVT (Power, Voltage &amp; Temperature) monitoring</li> </ul>
<b>Software</b>	<ul style="list-style-type: none"> <li>• Yocto-based Linux build system</li> <li>• Boot process and Linux device driver</li> <li>• Reference Linux BSP for the evaluation Board</li> <li>• DDR tuning S/W</li> <li>• Debug solutions (GDB/TRACE32)</li> </ul>

## SoC Platform Engagement Models

### Max Efficiency Model

By reusing the platform architecture and feature subblock tailored for this domain, customers can focus on their differentiation and maximize efficiency of SoC development. Perfect for SoC prototyping or high-value applications that require super-fast time-to-market speed.

### Max Flexibility Model

SEMIFIVE Platform technology offers flexibility to configure the platform architecture to your application's special requirements. SEMIFIVE can also develop new subblock design to add features necessary for the application. It's the best way to explore the trade-offs between time and cost.